Amdt. dated 26 March 2008

Reply to Office Action of 26 November 2007

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for decoding a received sequence of symbols of a

frame using a turbo decoding process that comprises a plurality of decoder iterations, the method

comprising:

determining whether a pre-determined decoder termination threshold metric has been

met;

identifying frames for use in further processing if the threshold metric has been met

but only after a pre-determined number of decoder iterations marking the frame as potentially

inaccurate for further processing and passing the frame for cyclic redundancy check testing, or if the

threshold metric has been met before the pre-determined number of decoder iterations passing the

frame unmarked for cyclic redundancy check testing;

only if the threshold metric has been met, determining whether a decoder termination

test based on a cyclic redundancy check code has been passed; and

only if the cyclic redundancy check test has been passed, terminating the decoder

iterations.

2. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises determining whether a cross-entropy between a distribution

of log-likelihood ratios for each decoder iteration is less than a pre-determined cross-entropy

threshold.

Amdt, dated 26 March 2008

Reply to Office Action of 26 November 2007

3. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises using a sign change ratio to monitor convergence of the

decoding process.

4. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises using a sign difference ratio to monitor convergence of the

decoding process.

5. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises using a hard-decision aided test.

6. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises using an average absolute log-likelihood ratio.

7. (Original) A method according to claim 1, wherein determining whether the

threshold metric has been met comprises determining whether an absolute value of a smallest log-

likelihood ratio is above a pre-determined absolute value threshold.

8. (Original) A method according to claim 1, further comprising receiving the

sequence of symbols from a parallel turbo encoder.

9. (Original) A method according to claim 1, further comprising receiving the

sequence of symbols from a serial turbo encoder.

10. (Original) A method according to claim 1, further comprising receiving the

sequence of symbols from a transmitter comprising an encoder and a modulator.

11. (Original) A method according to claim 1, wherein the method comprises

decoding symbols in a wireless cellular system.

Amdt. dated 26 March 2008

Reply to Office Action of 26 November 2007

12. (Original) A method according to claim 11, wherein the wireless cellular

system comprises a W-CDMA transmitter and a W-CDMA receiver.

13. (Currently Amended) An apparatus for decoding a received sequence of

symbols of a frame using a turbo decoding process that comprises a plurality of decoder iterations,

the apparatus comprising:

a threshold metric processor for determining whether a pre-determined decoder

termination threshold metric has been met;

a maximum iteration processor for identifying frames for use in further processing if

the threshold metric has been met but only after a pre-determined number of decoder iterations

marking the frame as potentially inaccurate for further processing and for passing the frame for

cyclic redundancy check testing at a cyclic redundancy check processor, or if the threshold metric has

been met before the pre-determined number of decoder iterations for passing the frame unmarked for

cyclic redundancy check testing at the cyclic redundancy check processor;

a cyclic redundancy check processor for determining, only if the threshold metric

processor determines that the threshold metric has been met, whether a decoder termination test

based on a cyclic redundancy check code has been passed; and

a decoder termination means for terminating the decoder iterations, only if the cyclic

redundancy check test has been passed.

14. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for determining whether a cross-entropy between a distribution of log-

likelihood ratios for each decoder iteration is less than a pre-determined cross-entropy threshold.

Serial No.: 10/825,212 Amdt. dated 26 March 2008

Reply to Office Action of 26 November 2007

15. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for using a sign change ratio to monitor convergence of the decoding

process.

16. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for using a sign difference ratio to monitor convergence of the decoding

process.

17. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for using a hard-decision aided test to monitor convergence of the

decoding process.

18. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for using an average absolute log-likelihood ratio to determine whether

the threshold metric has been met.

19. (Original) An apparatus according to claim 13, wherein the threshold metric

processor comprises means for determining whether an absolute value of a smallest log-likelihood

ratio is above a pre-determined absolute value threshold.

20. (Original) An apparatus according to claim 13, further comprising means for

receiving the sequence of symbols from a parallel turbo encoder.

21. (Original) An apparatus according to claim 13, further comprising means for

receiving the sequence of symbols from a serial turbo encoder.

22. (Original) An apparatus according to claim 13, further comprising means for

receiving the sequence of symbols from a transmitter comprising an encoder and a modulator.

23. (Original) An apparatus according to claim 13, wherein the apparatus

comprises a wireless cellular system.

Amdt. dated 26 March 2008

Reply to Office Action of 26 November 2007

24. (Original) An apparatus according to claim 23, wherein the apparatus comprises a W-CDMA transmitter and a W-CDMA receiver.

25. (Currently Amended) A computer program product provided on a computerreadable medium and comprising program code means adapted to <u>eontrol perform</u> the method of claim 1.